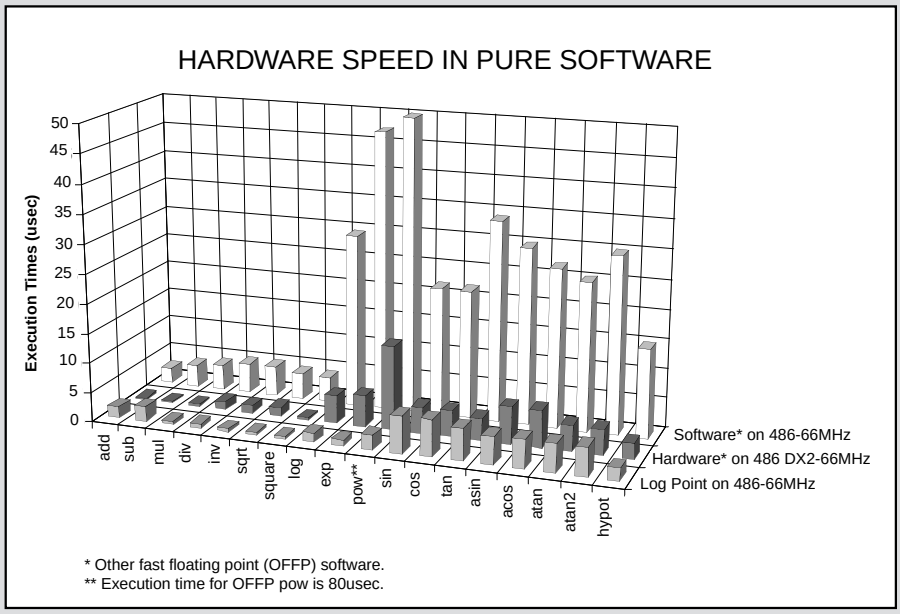
SuperSpeed* Floating Point Math Products

- Fast Floating Point Software
- Outperforms Other Floating Point Software
- Performance Comparable to Hardware Solutions
- Complement Embedded Intel Architecture Hardware
- ROMable and Re-Entrant
- Same Range, Precision as IEEE 754
- Fast Divide, Roots, Powers, Other Functions
- Support Borland, Microsoft, GNU Compilers
- Support MS-DOS and Windows



SuperSpeed* math software products provide compact, economical, and easy-to-use routines that give high level floating point functionality, without hardware constraints. SuperSpeed math provides performance exceeding the fastest conventional floating point software.

Log Point SuperSpeed Soft CoProcessors are compact, high performance numerical processors that constitute the first general purpose exponential floating point (efp) computational environment. The new character of efp computation gives Log Point math products unrivaled performance and flexibility.

Unlike conventional floating point, efp is based upon the combination of a fully logarithmic data format and ultra high speed logarithmic and exponential data transformations. As a result, many tough nonlinear computations are simplified and enhanced.

With the new efp data format, numerical computation is still directly equivalent to conventional floating point (cfp) using the same size data format to provide virtually identical precision and dynamic range. This equivalence allows existing cfp algorithms to achieve significantly improved performance without modification and also provides ample opportunity for further enhancement.

ROMable re-entrant code forms include: C source, optimized assembler source and linkable object modules; libraries compatible with Microsoft Visual C, Borland C/C++, Metaware High C, Watcom C/C++, and GNU Tools. Soft Coprocessors are thoroughly tested to deliver uniform precision with comprehensively documented timing and error characteristics.

Execution times for the Soft CoProcessor in the 3-D plot are for C source. In optimized assembler source, all functions would be faster than C source by 30% or more. If the application is always within dynamic range, further optimization can be obtained (multiply/divide would take 0.015 usec, square root would take 0.030 usec, other function execution times would be reduced by 30% or more).

HOST SYSTEMS SUPPORTED:
MS-DOS and Windows

PROCESSORS SUPPORTED:
80186, 80C186, 80C186XL/EA/EB/EC, 80L186EA/EB/EC, 80188, 80C188, 80C188XL/EA/EB/EC, 80L188EA/EB/EC, Intel386™ CX/EX/SX/SXSA/DX, Intel486™ SX, IntelDX2™, and IntelDX4™ processors

AVAILABILITY:
Now

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